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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/697,457	10/30/2003	Craig M. Perlov	10005727-8	7988
7590	11/26/2004		EXAMINER	
HEWLETT-PACKARD COMPANY Intellectual Property Administration P.O. Box 272400 Fort Collins, CO 80527-2400			GUERRERO, MARIA F	
			ART UNIT	PAPER NUMBER
			2822	

DATE MAILED: 11/26/2004

Please find below and/or attached an Office communication concerning this application or proceeding.

<b>Office Action Summary</b>	<b>Application No.</b>	<b>Applicant(s)</b>
	10/697,457	PERLOV ET AL.
Examiner	Maria Guerrero	Art Unit 2822

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

### **Period for Reply**

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If the period for reply specified above is less than thirty (30) days, a reply within the statutory minimum of thirty (30) days will be considered timely.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

## Status

1)  Responsive to communication(s) filed on 25 October 2004.

2a)  This action is **FINAL**.                            2b)  This action is non-final.

3)  Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

## **Disposition of Claims**

4)  Claim(s) 19-29 is/are pending in the application.  
4a) Of the above claim(s) \_\_\_\_\_ is/are withdrawn from consideration.

5)  Claim(s) \_\_\_\_\_ is/are allowed.

6)  Claim(s) 19,20 and 22-29 is/are rejected.

7)  Claim(s) 21 is/are objected to.

8)  Claim(s) \_\_\_\_\_ are subject to restriction and/or election requirement.

## Application Papers

9)  The specification is objected to by the Examiner.

10)  The drawing(s) filed on \_\_\_\_\_ is/are: a)  accepted or b)  objected to by the Examiner.

Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).

Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).

11)  The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

**Priority under 35 U.S.C. § 119**

12)  Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).  
a)  All    b)  Some \* c)  None of:  
1.  Certified copies of the priority documents have been received.  
2.  Certified copies of the priority documents have been received in Application No. \_\_\_\_\_.  
3.  Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).

\* See the attached detailed Office action for a list of the certified copies not received.

**Attachment(s)**

1)  Notice of References Cited (PTO-892)  
2)  Notice of Draftsperson's Patent Drawing Review (PTO-948)  
3)  Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08)  
Paper No(s)/Mail Date .  
4)  Interview Summary (PTO-413)  
Paper No(s)/Mail Date. \_\_\_\_ .  
5)  Notice of Informal Patent Application (PTO-152)  
6)  Other: .

## **DETAILED ACTION**

1. This Office Action is in response the preliminary amendment filed October 30, 2003.

### **Status of Claims**

2. Claims 1-18 are canceled. Claims 19-29 are pending.

### ***Specification***

3. The lengthy specification has not been checked to the extent necessary to determine the presence of all possible minor errors. Applicant's cooperation is requested in correcting any errors of which applicant may become aware in the specification.

### ***Information Disclosure Statement***

4. The information disclosure statement (IDS) submitted on October 25, 2004 has been considered.

### ***Claim Rejections - 35 USC § 102***

The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless –

(b) the invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United States.

5. Claims 19-20 and 22-29 are rejected under 35 U.S.C. 102(b) as being anticipated by Smith et al. (U.S. 5,224,023).

Smith et al. shows assembling a common substrate having multiple sections (Abstract). Smith et al. teaches constructing at least one fold line on the substrate to separate the multiple sections (Fig. 1-2, col. 1, lines 35-45). Smith et al. discloses fabricating memory structure on at least two sections of the substrate (Abstract, col. 4, lines 10-15). Smith et al. shows folding the substrate along the fold line to stack the multiple sections on top of each other and align the memory structure on adjacent folded sections to form at least one operable electrical device (Abstract). Smith et al. teaches including conductor grids perpendicular to each other (Fig. 1-3, col. 2, lines 1-68), col. 1, lines 58-678, col. 2, lines 1-20). Smith et al. discloses applying multiple aligned perforations and forming three separate sections capable of folding to a stacked layer configuration (Fig. 1-3, col. 2, lines 1-25). Smith et al. shows the sections being folded so that a center section of the substrate becomes a center layer of the folded sections and two fold lines are parallel to each other and at least two fold line are not parallel to each other (Fig. 1-3, col. 2, lines 1-20).

In addition, the elements must be arranged as required by the claim, but this is not an ipsissimis verbis test, i.e., identity of terminology is not required. *In re Bond*, 910 F.2d 831, 15 USPQ2d 1566 (Fed. Cir. 1990).

Furthermore, during patent examination, the pending claims must be "given \* >their< broadest reasonable interpretation consistent with the specification." > *In re Hyatt*, 211 F.3d 1367, 1372, 54 USPQ2d 1664, 1667 (Fed. Cir. 2000). While the claims of issued patents are interpreted in light of the specification, prosecution history, prior art and other claims, this is not the mode of claim interpretation to be applied during

examination. During examination, the claims must be interpreted as broadly as their terms reasonably allow. > In re American Academy of Science Tech Center, F.3d, 2004 WL 1067528 (Fed. Cir. May 13, 2004)(The USPTO uses a different standard for construing claims than that used by district courts; during examination the USPTO must give claims their broadest reasonable interpretation.) < This means that the words of the claim must be given their plain meaning unless applicant has provided a clear definition in the specification. In re Zletz, 893 F.2d 319, 321, 13 USPQ2d 1320, 1322 (Fed. Cir. 1989) >; Chef America, Inc. v. Lamb-Weston, Inc., 358 F.3d 1371, 1372, 69 USPQ2d 1857 (Fed. Cir. 2004).

#### ***Allowable Subject Matter***

6. Claim 21 is objected to as being dependent upon a rejected base claim, but would be allowable if rewritten in independent form including all of the limitations of the base claim and any intervening claims.

The following is a statement of reasons for the indication of allowable subject matter: in the examiner's opinion, it would have not been obvious to a person of ordinary skill in the art at the time of the invention to modify the cited references in order to meet the limitations: fabricating diode fuse patterns on at least one of the two sections and aligning the two sections so that the diode fuse patterns coincide to form a matrix of diode fuses in combination with the rest of limitations in the claims because there is not motivation or suggestion.

***Conclusion***

7. The prior art made of record and not relied upon is considered pertinent to applicant's disclosure. Tagawa (U.S. 4,287,525), Schmidt et al. (U.S. 5,008,496), Pan (U.S. 6,142,611), Craig (U.S. 5,792,943), Schantz et al. (EP 0564295 A1), and Shim et al. (U.S. 6,683,377) teach several steps related to applicant's disclosure.

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Maria Guerrero whose telephone number is 571-272-1837.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Amir Zarabian can be reached on 571-272-1852. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

November 22, 2004

*Maria Guerrero*  
MARIA F. GUERRERO  
PRIMARY EXAMINER